10/511994

Title: ELECTROMAGNETICALLY SHIELDED CIRCUIT DEVICE AND SHIELDING METHOD THEREOF

Inventor(s): Toshinobu OGATSU Filing Date: October 21, 2004 Serial No.: To be assigned

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Fig. 1

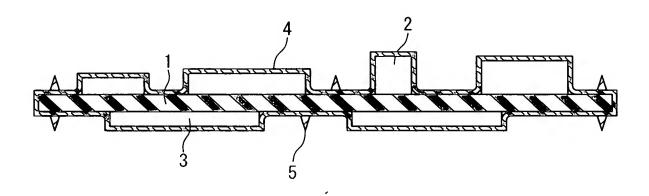
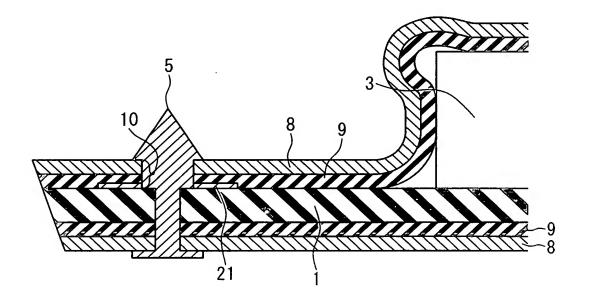


Fig. 2



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Fig. 3

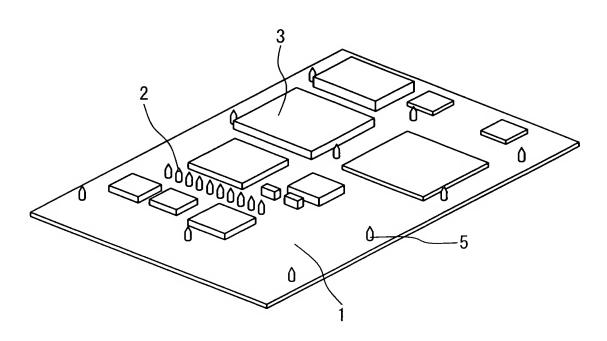
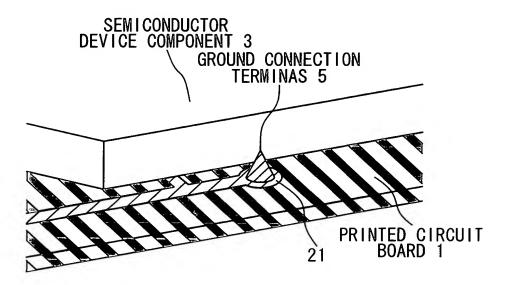


Fig. 4



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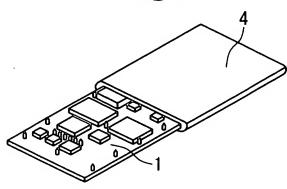


Fig. 5B

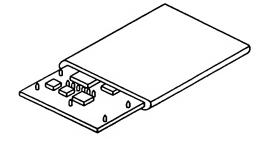


Fig. 5C

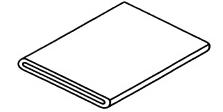
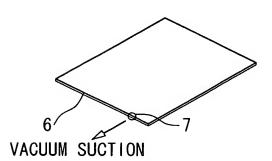


Fig. 5D



Title: ELECTROMAGNETICALLY SHIELDED CIRCUIT DEVICE AND SHIELDING METHOD THEREOF

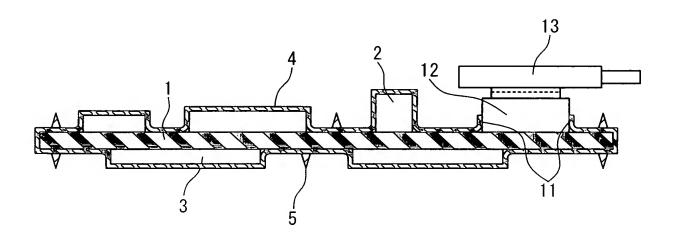
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Fig. 6



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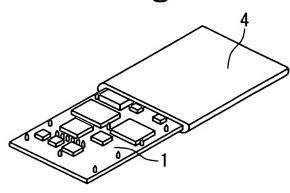


Fig. 7B

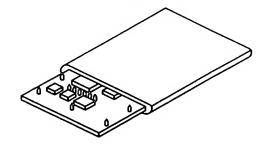
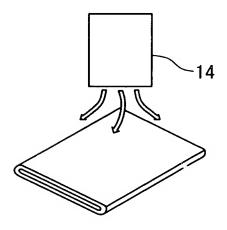


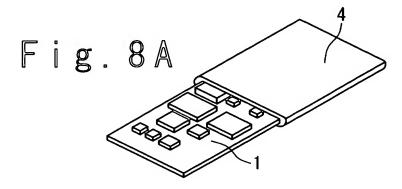
Fig. 7C

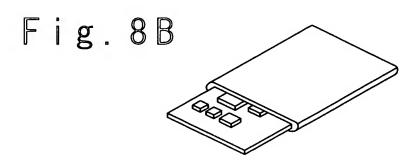


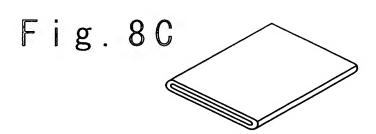
10/511994

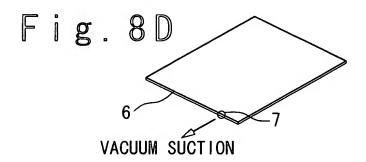
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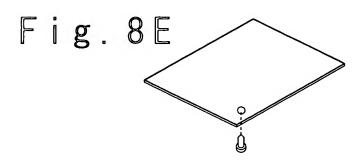
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## Fig. 9

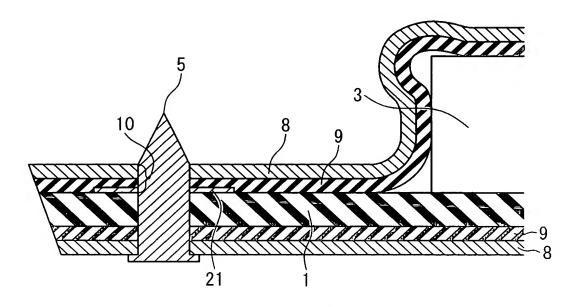


Fig. 10

